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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/707,646	12/30/2003	Chun-Huai Li	ADTP0085USA	1645
27765	7590	05/30/2006	EXAMINER	
NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION P.O. BOX 506 MERRIFIELD, VA 22116				NGUYEN, KEVIN M
		ART UNIT		PAPER NUMBER
		2629		

DATE MAILED: 05/30/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/707,646	LI, CHUN-HUAI	
	<b>Examiner</b>	<b>Art Unit</b>	
	Kevin M. Nguyen	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 30 December 2003.
- 2a) This action is FINAL.                            2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-18 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 30 December 2003 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____.

**DETAILED ACTION**

***Specification***

1. The preliminary amendment to the title of the invention filed 05/28/2004 is acknowledged. That said title is approved.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-18 are rejected under 35 U.S.C. 102(b) as being anticipated by Friend et al (US 6,429,601) hereinafter Friend.

3. As to claim 1, Friend teaches a pixel structure of an active matrix display device [Fig. 5], the active matrix display device having a source of first potential [12] and a source of second potential [20], the pixel structure comprising:

a storage capacitor [14a];

a first active device [13a] having a first end electrically connected to a scanning line [10], a second end electrically connected to a data line [11a], and a third end electrically connected to the storage capacitor [14a]; and

a plurality of active-type light emitting devices [19a, 19b, 19c, 19d] connected in parallel with each other, each of the active-type light emitting devices [19a, 19b, 19c, 19d] being electrically connected between the source of first potential [12], the source of second potential [20], and the third end [an end of the capacitor 14a, see Fig. 5, col. 4, lines 34-46, and col. 5, lines 34-60].

As to claim 2, Friend teaches the pixel structure of claim 1, wherein the first active device [14a, Fig. 5] is a first thin film transistor<sup>1</sup> [TFT], and the first end is a gate electrode of the first thin film transistor, the second end is a drain electrode of the first thin film transistor, and the third end is a source electrode of the first thin film transistor [the thin-film transistor 7 corresponding to the TFT 14a which includes a gate electrode, a source electrode, and drain electrode<sup>2</sup>, see col. 1, lines 50-64, col. 4, lines 3-14, and col. 5, lines 44-46].

As to claim 3, Friend teaches the pixel structure of claim 1, wherein the storage capacitor [14a] is electrically connected between the third end and a source of constant potential [12] that is utilized for supplying a constant potential [see Fig. 5, col. 5, lines 37-40, and col. 1, lines 53-56].

As to claim 4, Friend teaches the pixel structure of claim 3, wherein the source of constant potential is the source of first potential [12, see Fig. 5, col. 5, lines 37-40, and col. 1, lines 53-56].

As to claim 5, Friend teaches the pixel structure of claim 1, wherein each of the active-type light emitting devices comprises: a second active device [15a, Fig. 5] having

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<sup>1</sup> See [http://en.wikipedia.org/wiki/Thin\\_film\\_transistor](http://en.wikipedia.org/wiki/Thin_film_transistor)

a fourth end connected to the third end, a fifth end connected to the source of first potential [12], and a sixth end; and a light emitting device [19a] having a seventh end connected to the sixth end and an eighth end connected to the source of second potential [20, see Fig. 5, col. 4, lines 34-46, and col. 5, lines 34-60].

As to claim 6, Friend teaches the pixel structure of claim 5, wherein when an electrical shortage occurs in one of the light emitting devices, the pixel structure displays an image via the other light emitting devices [see col. 6, lines 27-51].

As to claim 7, Friend teaches the pixel structure of claim 5, wherein each of the second active devices [15a, 15b, 15c, 15d] comprises a second thin film transistor [19b, see Fig. 5, col. 4, lines 34-46, and col. 5, lines 34-60].

As to claim 8, Friend teaches the pixel structure of claim 7, wherein the fourth end is a gate electrode of the second thin film transistor [15b], the fifth end is a source electrode of the second thin film transistor [15b], and the sixth end is a drain electrode of the second thin film transistor [15b, see Fig. 5. It is noted that each of TFT 15 has a gate electrode, a source electrode, and drain electrode].

As to claim 9, Friend teaches the pixel structure of claim 5, wherein each of the light emitting devices comprises an organic light emitting diode (OLED) [see col. 3, lines 41-53].

As to claim 10, Friend teaches the pixel structure of claim 9, wherein the seventh end is an anode of the light emitting device, and the eighth end serves as a cathode of

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<sup>2</sup> See [http://en.wikipedia.org/wiki/Field-effect\\_transistor](http://en.wikipedia.org/wiki/Field-effect_transistor)

the light emitting device [it is noted that at least a OLED 19a has an anode and a cathode, see Fig. 15, col. 5, lines 51-57].

4. As to claim 11, Friend teaches an active matrix display device [Figs. 5 and 6] comprising:

referring to fig. 6, a plurality of scanning lines [25];

a plurality of data lines [26];

a plurality of pixels [22, 23, see Fig. 6, col. 6, lines 26-51], each of the pixels comprising:

referring to Fig. 5, a storage capacitor [14a];

a first active device [13a] having a first end electrically connected to the corresponding scanning line [10], a second end electrically connected to the corresponding data line [11a], and a third end electrically connected to the storage capacitor [14a]; and

a plurality of active-type light emitting devices [19a, 19b, 19c, 19d] electrically connected in parallel with each other, each of the active-type light emitting devices being connected between a source of first potential [12], a source of second potential [20], and the third end [an end of the capacitor 14a], each of the active-type light emitting devices [19a, 19b, 19c, 19d] comprising:

a light emitting device [19a] electrically connected to the source of second potential [20]; and

a second active device [15a] having a fourth end electrically connected to the third end, a fifth end electrically connected to the source of first potential [12], and a

sixth end electrically connected to the light emitting device [19a, col. 4, lines 34-46, and col. 5, lines 34-60 ].

The limitation of claim 12 is the same as those of claim 2 and therefore the claim will be rejected using the same rationale.

The limitation of claim 13 is the same as those of claim 3 and therefore the claim will be rejected using the same rationale.

The limitation of claim 14 is the same as those of claim 4 and therefore the claim will be rejected using the same rationale.

The limitation of claim 15 is the same as those of claim 5 and therefore the claim will be rejected using the same rationale.

The limitation of claim 16 is the same as those of claim 8 and therefore the claim will be rejected using the same rationale.

The limitation of claim 17 is the same as those of claim 9 and therefore the claim will be rejected using the same rationale.

The limitation of claim 18 is the same as those of claim 6 and therefore the claim will be rejected using the same rationale.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KEVIN M. NGUYEN whose telephone number is 571-272-7697. The examiner can normally be reached on MON-THU from 8:00-6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, a supervisor RICHARD A. HJERPE can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8000.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the Patent Application Information Retrieval system, see <http://portal.uspto.gov/external/portal/pair>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kevin M. Nguyen  
Patent Examiner  
Art Unit 2629

KMN  
May 24, 2006